



Docket No.: M4065.0741/P741

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Kit Fai Chan et al.

Confirmation No.: 8825

Application No.: 10/733,474

Allowed: September 14, 2006

Filed: December 12, 2003

Group Art Unit: 2186

For: CURRENT MODE LOGIC SCHEME
AND CIRCUIT FOR MATCHLINE
SENSE AMPLIFIER DESIGN USING
CONSTANT CURRENT BIAS
CASCODE CURRENT MIRRORS

Examiner: Shane M Thomas

**APPLICANT'S COMMENTS ON EXAMINER'S STATEMENT OF REASONS
FOR ALLOWANCE**

MS Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

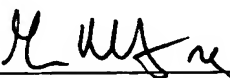
Dear Sir:

Applicants agree in part with the Examiner's statements of reasons for allowance, contained in the Office Action of May 3, 2006 and the Notice of Allowance of October 6, 2006 in that the prior art of record fails to disclose, teach or suggest the claimed invention. However, Applicants note that the Examiner's statement fails to address all limitations of all of the allowed claims. For example, certain claim limitations were not discussed in the Examiner's Reasons for Allowance, including those in most of the allowed claims, each of which defines a unique combination of features not shown or suggested by the prior art, providing additional reasons for allowance of each claim.

Furthermore, Applicants respectfully submit that the scope of the claimed invention should not be limited by the Examiner's statement, but rather the scope of the claimed invention should be based on the actual language of the allowed claims.

Dated: November 2, 2006

Respectfully submitted,

By  #41,198

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